

RESPONSE TO RESTRICTION REQUIREMENT AND AMENDMENT
Application No. 10/749,562

AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions and listings of claims in the application:

LISTING OF CLAIMS:

1 - 7 (canceled).

8. (original): A method for preparing an electric double layer capacitor, said method comprising steps of:

(A) applying a current collector having a plate-like form onto a surface of a substrate in such a manner that an uncovered portion of the substrate is formed around the current collector;

(B) disposing a gasket element in the form of a frame having an opening for accommodating the current collector on the uncovered portion of the substrate in such a manner that the current collector laterally abuts on the gasket element to define a concavity;

(C) placing a polarizable electrode impregnated with an electrolytic solution in the concavity defined by the current collector and the gasket element;

(D) oppositely disposing two intermediate products prepared through the steps (A) to (C) on a separator having a substantially planar form in such a manner that the gasket elements abut on surfaces of a peripheral portion of the separator;

(E) fusion-bonding the gasket elements together to laterally cover the separator with the resulting unified gasket to unite the two intermediate products and the separator; and

(F) removing the substrates from the current collectors and the gasket.

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9. (original): A method for preparing an electric double layer capacitor according to claim 8, wherein the substrate has a vapor transmission rate of about 10mg/m²/24h or lower measured in accordance with JIS Z 0208 test method when provided that diluted sulfuric acid is used as an electrolytic solution.

10. (original): A method for preparing an electric double layer capacitor according to claim 8, wherein the substrate is a polyethylene terephthalate film.

11. (original): A method for preparing an electric double layer capacitor according to claim 8, wherein the current collectors have a glass transition temperature higher than that of the substrate, said method further comprises between the steps (E) and (F), steps of:

(a) placing the product resulting from the step (E) in an environment having a temperature which is equal to the glass transition temperature of the substrate or higher and which is lower than the glass transition temperature of the current collector; and subsequently

(b) placing the resultant in an environment having a temperature which is room temperature or lower.

12. (original): A method for preparing an electric double layer capacitor according to claim 8, wherein a plurality of current collectors are applied onto the substrate in the step (A) and wherein the current collectors on the substrate in the step (A), the resulting products on the

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substrate from each of the steps (B) and (C) and the resulting products on between the substrates from the step (D) are en bloc subjected to the next steps (B) to (E), respectively.